

X-735 US
09/684,529

PATENT
Conf. No. 1502



IN THE UNITED STATES PATENT OFFICE

Applicants: John D. Logue, et al.
Assignee: Xilinx, Inc.
Title: Synchronized Multi-Output Digital Clock Manager
Serial No: 09/684,529 Filed: October 6, 2000
Examiner: Eva Y. Zheng Art Unit: 2634
Docket No: X-735 US Conf. No.: 1502

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION AFTER RCE

Dear Sir:

Applicants submit the following amendment in response to the First Office
Action after the RCE mailed on February 8, 2005.

Amendments to the Claims are reflected in the listing of claims which begins
on page 2 of this paper.

Remarks begin on page 6 of this paper.